

Table of contents

| | |
|---------------------------------|---|
| Thursday 14 November 2013 | 1 |
|---------------------------------|---|

International Training Workshop on FPGA Design for Scientific Instrumentation and Computing | (smr 2499)

Thursday 14 November 2013

Combinational and Sequential Logic Described in VHDL - Description, Synthesis and Simulation - (15:30-18:30)

| time | title | presenter |
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| 15:30 | Combinational and Sequential Logic Described in VHDL - Description, Synthesis and Simulation | KRISHNA M. KHARE, THULASIRAMAN NANDHA KUMAR, MIGUEL A. RISCO CASTILLO, CRISTIAN A. SISTERNA , CARLOS F. SOSA PAEZ |