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International Training Workshop on FPGA Design for Scientific Instrumentation and Computing | (smr 2499)

Wednesday 20 November 2013

Digital Signal Processing with FPGA. PMOD Modules (AD, DA, BNC). Design Example. - (16:30-17:30)

time	title	presenter
16:30	Digital Signal Processing with FPGA. PMOD Modules (AD, DA, BNC). Design Example.	CARLOS SOSA PAEZ