

International Training Workshop on FPGA Design for Scientific
Instrumentation and Computing | (smr 2499)

Contribution ID : 50

Type : **not specified**

Digital Signal Processing with FPGA

Thursday, 21 November 2013 17:30 (1:30)

Content

Summary

Primary author(s) : THULASIRAMAN NANDHA KUMAR, MIGUEL A. RISCO CASTILLO, CRISTIAN A. SISTERNA , CARLOS F. SOSA PAEZ

Presenter(s) : THULASIRAMAN NANDHA KUMAR, MIGUEL A. RISCO CASTILLO, CRISTIAN A. SISTERNA , CARLOS F. SOSA PAEZ

Session Classification : Digital Signal Processing with FPGA