



The Abdus Salam
**International Centre
for Theoretical Physics**
50th Anniversary 1964–2014



Latin-American Conference and FPGA School on Advanced Instrumentation

1 - 19 December 2014
Santa Clara – Costa Rica

The Abdus Salam International Centre for Theoretical Physics (ICTP, Italy) and the Costa Rica Institute of Technology (TEC, Costa Rica) will jointly organize a *Latin-American FPGA School on Advanced Instrumentation*, to be held in Santa Clara, Costa Rica, from 1 to 19 December 2014. The last three days of the school will be dedicated to the first regional Conference on Advanced Instrumentation (CRIA 2014) to be also held at Costa Rica TEC from 17 to 19 December 2014. Specific information about the Conference can be found at <http://cria2014.instrumentacionavanzada.net/>

PURPOSE OF THE SCHOOL

The aim of school is to introduce physicists, engineers, computer scientists and other scientists to FPGA design methodologies focusing applications in scientific instrumentation. The school will represent a unique opportunity for many of the participants to gain skills for designing and developing complete high performance systems for scientific applications at low cost. Since the main barrier to exploit this technology is knowledge, this school will contribute to generate know how through hands-on experimental activities with state of the art hardware and software. The participants will count on international highly qualified lecturers and laboratory assistants who are selected professionals from academy, research centers and industry. This will contribute to provide a balanced perception and deeper understanding about limitations, trends and potentialities of this key technology.

The School will contain about 35 hours of lectures and 35 hours of tutorials and assisted hands on laboratory sessions. During the last three days of the school, the selected applicants will also participate to the first regional conference on advanced instrumentation (CRIA 2014).

It is expected to enhance the capabilities of researchers, teachers, and engineers in exploiting cost-effective programmable logic devices for their instrumentation needs and R&D for scientific and academic purposes. By promoting networking and open international collaborations and by stimulating a community of experimental scientists with the necessary skills and willingness to contribute to collaborative open hardware/software research projects, this school will contribute to alleviate the lack of stimuli and isolation that typically afflicts many professionals from developing countries.

PARTICIPATION

Applicants from Latin-American countries that are members of the United Nations, UNESCO or IAEA may attend. As the School will be conducted in Spanish and English, participants should have an adequate working knowledge of these languages.

As a rule, travel and subsistence expenses of the participants are borne by the home institution. Every effort should be made by candidates to secure support for their fare (or at least half fare) from their home country. However, limited funds are available for some participants who are nationals of, and working in a developing country, and who are not more than 45 years old. Such support is available only for those who attend the entire activity. **There is no registration fee to attend the School.**

HOW TO APPLY

The application form can be accessed at the School website:
http://cdsagenda5.ictp.trieste.it/full_display.php?ida=a13261

Once in the website, comprehensive instructions will guide you step-by-step, on how to fill out and submit online the application form **not later than 03 August 2014**. Recommendation letters are not mandatory, but may help you in the admission process.

Secretariat: e-mail: smr2641@ictp.it phone: +39-040-22409911 fax: +39-040-2240-7932
School's web page: http://cdsagenda5.ictp.trieste.it/full_display.php?ida=a13261 ICTP Home Page: www.ictp.it

May 2014



TEC | Tecnológico
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**APPLICATION
DEADLINE**

03 August 2014